WarpAttack: Bypassing CFI through Compiler-Introduced Double-Fetches

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Course of ICT Risk Assessment





• Introduce the vulnerability

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- Introduce the vulnerability
- Present the mechanism underlying the attack and practical PoC

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- Evaluation and mitigations

1 Introduction

- 2 Background
- OProof of Concept
- **4** Gadget Code Detection
- **5** Evaluation
- **6** Mitigations

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Image: A matrix and a matrix



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ASLR



- ASLR
- Canaries



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- Canaries
- CFI



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- ASLR
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- CFI

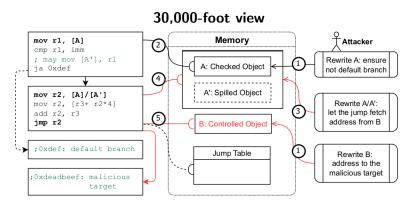
Not a total solution!

Compiler-introduced double-fetch of a bound-checked indirect jump with a jump table



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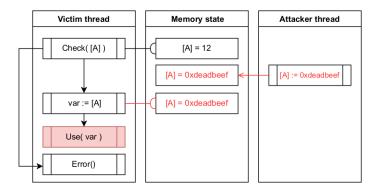
"Double-fetch bugs occur when a privilege system reads a variable multiple times, but the fetched value is inconsistent due to concurrency issues"



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"Double-fetch bugs occur when a privilege system reads a variable multiple times, but the fetched value is inconsistent due to concurrency issues"

- CVE-2008-2252: Windows
- CVE-2005-2490: Linux kernel
- CVE-2015-1420: Linux kernel (Android)
- CVE-2022-48357: Huawei products



The code for a jump table lookup consists of

- a bound check;
- an indirect jump (whose address is computed with the checked value).

```
1 ;switch(obj->type) {
2 ; case 0:
3 ; ...
4 ; default:
5 ; ...
6 ;}
7 mov rax, rdi
8 mov eax, DWORD PTR [rdi+0x30]
9 add eax, 0xfffffff
10 cmp eax, 0x11 ;the bound check
11 ja 401163 ;default branch
12 lea rdi, [rax+0x30]
13 jmp QWORD PTR [rax*8+0x402008]
```



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• Arbitrary read-write

Defensive Assumptions



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Marco Antonio Corallo (UniPi)

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- Thread control

Defensive Assumptions



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- Gadgets: switch jump table with a compiler-introduced double-fetch

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Defensive Assumptions

- Non-Executable Memory
- Randomization
- Control Flow Protection

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Image: Image:

The victim: a complex and realistic target available for all common operating systems



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The victim: a complex and realistic target available for all common operating systems



Version: 106.0.1 Built by: GCC 12.1.1 • Gain arbitrary read/write capability



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Image: A matrix and a matrix

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• Gain arbitrary read/write capability: CVE-2022-26485



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Image: Image:

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• Gain arbitrary read/write capability: out-of-bound that grants arbitrary read/write capabilities through *ArrayBuffers*.

- Gain arbitrary read/write capability
- Leak ASLR bases for both libxul.so and the stack

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PoC Exploit

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 - S the address of one writeable memory region: .bss section of libxul.so

out-of-bound <code>Uint8Array</code> \rightarrow <code>leak</code> <code>ArrayBuffer._elements</code> \rightarrow <code>libxul</code> base address \rightarrow <code>__environ</code> \rightarrow stack base address

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- Find double-fetch gadgets of bound-checked indirect jumps: *lightweight binary analysis tool*

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- Reaching gadget code: libxul's TraceJitActivation() document.getElementById('textarea').value += x



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- Reaching gadget code
- Orchestrate the thread scheduling to win the data race

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- Find double-fetch gadgets of bound-checked indirect jumps
- Reaching gadget code
- Orchestrate the thread scheduling to win the data race
- Overwrite the checked object and hijack the control flow

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Image: A matrix and a matrix

• designed for offensive purposes



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- designed for offensive purposes
- based on Radare2

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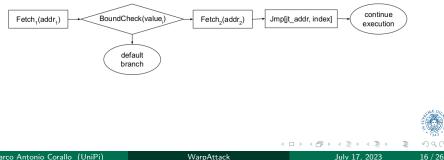
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Intel(R) Core(TM) i7-10700 CPU (8 cores) @ 2.90GHz with 32GB of memory and Fedora 36



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Algorithm 1 Measuring our PoC's success rate.			
1: function EXPERIMENT			
2: while 2000 times do			
3: repeat	⊳ one attempt		
4: attack()	▷ run the race to overwrite		
5: until 20s have passed			
6: end while			
7: end function			

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Intel(R) Core(TM) i7-10700 CPU (8 cores) @ 2.90GHz with 32GB of memory and Fedora 36

TABLE 1. DIFFERENT SUCCESS RATESBY TUNING NUMBER OF CORESAND NUMBER OF ATTACKER THREADS (IN 2000 RUNS).

#Como	#Att	acker Thr	eads
#Core	1	3	7
1	0	0	0
4	0.05%	0.25%	0.2%
8	0.15%	0.15%	0.45%

• Do compiler-introduced double-fetch gadgets exist in real programs?



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- Do compiler-introduced double-fetch gadgets exist in real programs?
- Which compiler is affected by such situation?
- Which CFI implementation is vulnerable to WarpAttack?
- What architectures are affected by WarpAttack?

Victim Code in the Wild

TABLE 2. Statistics of double-fetch gadgets in the wild. Weexcluded Apache for Mac OS because we fail to find thecorrect pre-compiled version for Intel Mac OS.

Program	Fedora	Debian	Ubuntu	Windows	Mac OS
Chrome	1024	16	23	24	16
Firefox	616	659	31	0	29
Apache	15	17	16	0	-
JVM	0	0	0	0	1
7-zip	24	24	24	0	0
Texstudio	8	9	9	230	20
Total	1687	725	103	254	66



TABLE 3. COMPILERS THAT CAN INTRODUCE EXPLOITABLE DOUBLE-FETCH PAIRED TO THEIR COMPILATION OPTIONS. THE SYMBOL "*" INDICATES CASES OBSERVED FROM REAL WORLD PROGRAMS.

Compiler	Option	double-fetch Type	version
GCC	01,02,03,0fast	Var. 1 (fetch-fetch)	12.1
*G++	01,02,03	Var. 2 (fetch-spill-fetch)	12.1
Clang	00	Var. 2 (fetch-spill-fetch)	14.0.*
Clang	01,02,03	Var. 2 (fetch-spill-fetch)	14.0.
Clang	03	Var. 1 (fetch-fetch)	14.0.
Clang++	03	Var. 1 (fetch-fetch)	14.0.
MSVC	Od	Var. 1 (fetch-fetch)	19.32.*

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TABLE 4. CFI IMPLEMENTATIONS VULNERABLE TO OUR ATTACK.

CFI Type	Compiler	Vulnerable CFI
Compiler-based CFI	GCC	VTV 13
	Clang	LLVM-CFI 12
	MSVC	CFG 43
Binary only CFI	_	Lockdown 20



TABLE 5. CONFIRMED VULNERABLE ARCHITECTURES AND INVOLVED VARIANTS AND COMPILERS.

	Variant 1 (fetch-fetch)	Variant 2 (fetch-spill-fetch)
X86/-64	GCC 01/02/03	Clang O0; MSVC Od
ARM 32/64	-	Clang O0; MSVC Od
RISCV 32/64	-	Clang O0
MIPS 32/64	GCC 01/02/03	-



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Image: Image:

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• Avoiding Gadget code generation

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• Avoiding Gadget code generation: GCC's -fno-switch-tables



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• Avoiding Gadget code generation: GCC's -fno-switch-tables Clang, MSVC optimization level > 00

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- Avoiding Gadget code generation: GCC's -fno-switch-tables Clang, MSVC optimization level > 00
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- Protecting Indirect Jump: dynamic checks
- Monitoring for Attack Behavior: spawning several threads, constantly writing a certain memory site; crashes; ...
- Making compilers aware of sensitive code: annotating security-related code

Based on the work of

J. Xu, L. Di Bartolomeo, F. Toffalini, B. Mao, M. Payer

Thank You.

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